



3.6 Watt BZ Series



Ultra Low Noise 12mVpp, Wide Temp -20°C to +70°C, Isolated Type DC-DC Converter

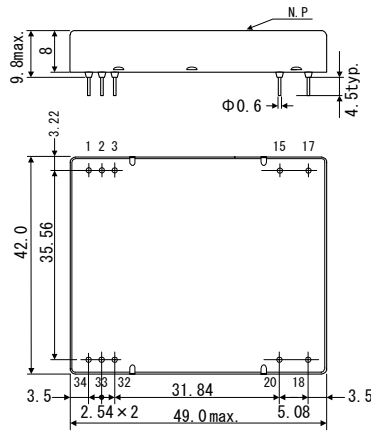
Input: 5V Output: 5V, 12V, 15V, ±12V, ±15V

- Analog, Digital Optimum
- MTBF 1,000,000Hrs
- Short Circuit, Over-Current Protection
- No Electrolytic Capacitor, No Tantalum Capacitor
- 5-Side Metallic shield structure
- EMI Line Filter
- Over-Heat Protection
- Long-Life with TCT Patent Circuit
- Isolation Voltage DC500V
- Low Drift 50mV/8H
- Operating Temp Range -20°C to +70°C (Temp Derating Required for 50°C)
- RoHS Compliance

Models BZ Series	Input V Vdc	Output V Vdc	Output I mA	Line Reg % (typ.)	Load Reg % (typ.)	Ripple/ Noise mVpp(typ.)	Efficiency % (typ.)
BZ05-12W15	4.75-6	±12	±0-150	0.3	0.3	12	60
BZ05-15W12	4.75-6	±15	±0-120	0.3	0.3	12	60
BZ05-05S-40	4.75-6	5	0-400	0.3	0.3	20	60
BZ05-12S-30	4.75-6	12	0-300	0.3	0.3	20	60
BZ05-15S-24	4.75-6	15	0-240	0.3	0.3	20	60

Note 1: Derating required for input voltage above 5.25V.

<Outline>

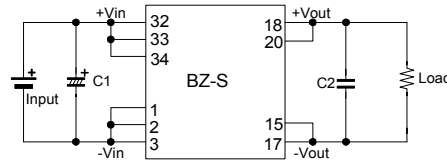


Pin	Function
1	-Vin
2	-Vin
3	-Vin
15	-Vout
17	-Vout
18	+Vout
20	+Vout
32	+Vin
33	+Vin
34	+Vin

Pin	Function
1	-Vin
2	-Vin
3	-Vin
15	-Vout
17	Common
18	Common
20	+Vout
32	+Vin
33	+Vin
34	+Vin

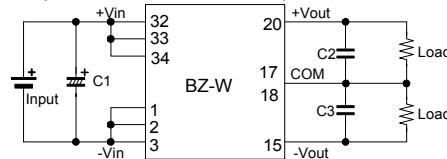
Units: mm
Weight: 20g typ.

<Standard Connection Diagram>



- Recommended Capacitor
C1=47µF-100µF (Electrolytic or multilayer ceramic capacitor)
C2=1µF (Multilayer ceramic capacitor)

External capacitors are not required, but noise can be lowered by reducing power line impedance and load line impedance.



- Recommended Capacitor
C1=47µF-100µF (Electrolytic or multilayer ceramic capacitor)
C2, C3=0.47µF-1µF (Multilayer ceramic capacitor)

External capacitors are not required, but noise can be lowered by reducing power line impedance and load line impedance.

- Note!
This catalogue is an outline of the products.
When designing, be sure to refer to the data sheets.